

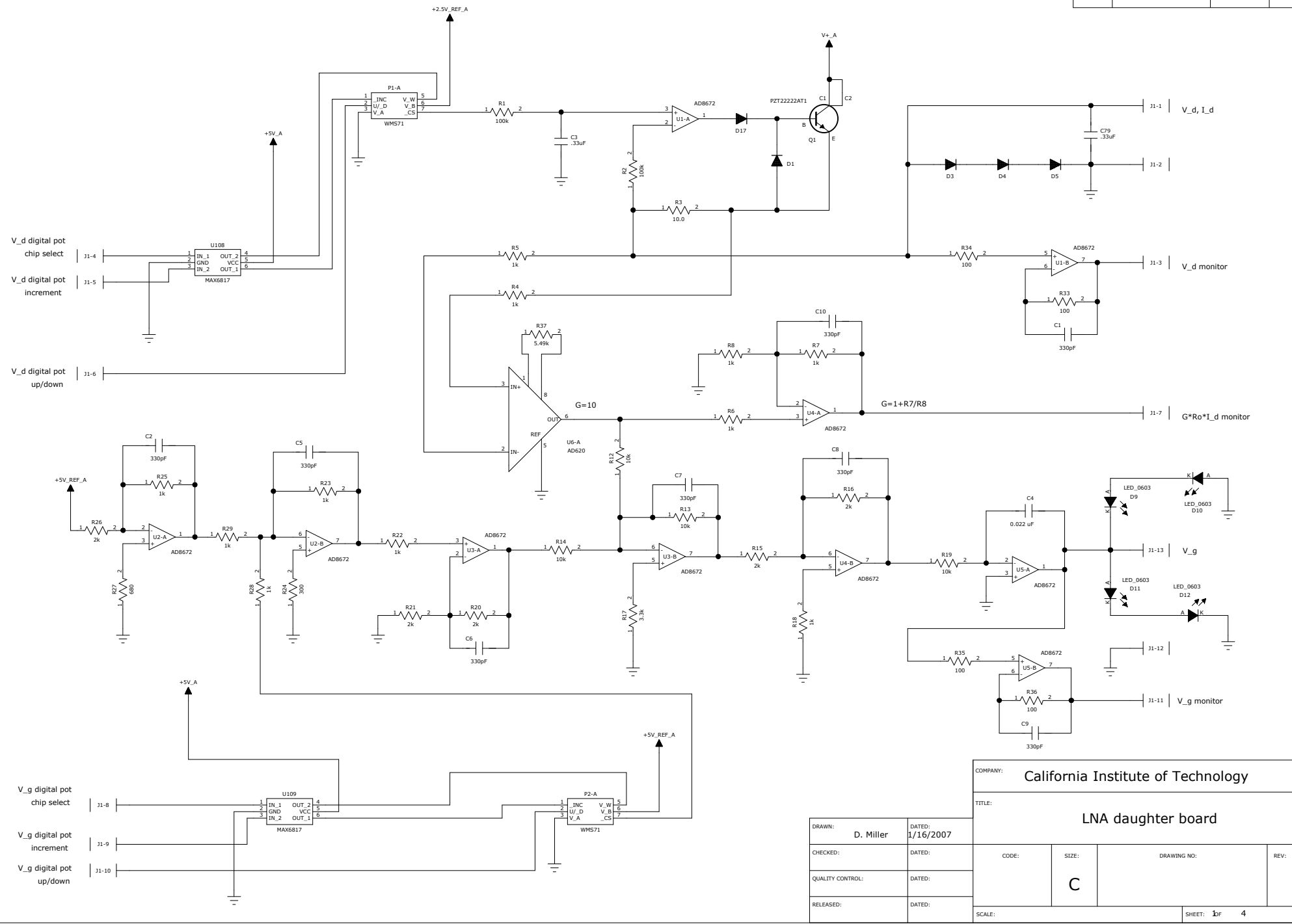
D

C

B

A

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: California Institute of Technology			
TITLE: LNA daughter board			
CODE:	SIZE:	DRAWING NO:	REV:
C			
SCALE:		SHEET: 1 of 4	

DRAWN: D. Miller	DATED: 1/16/2007
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

6

5

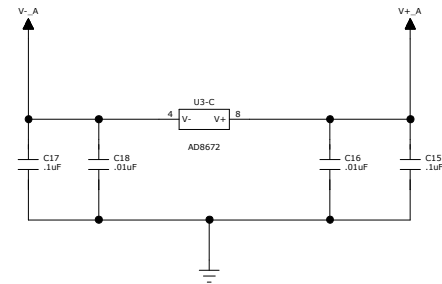
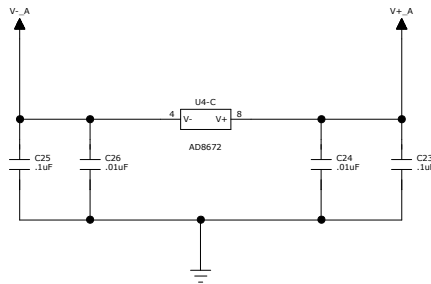
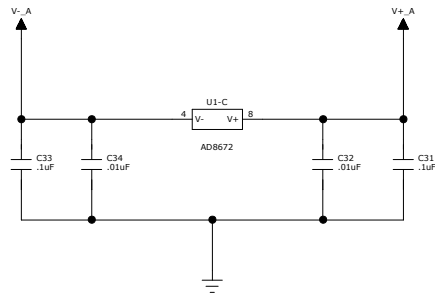
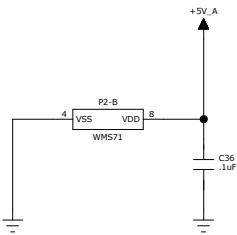
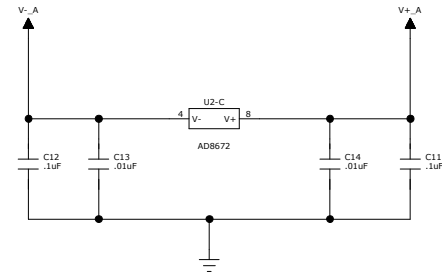
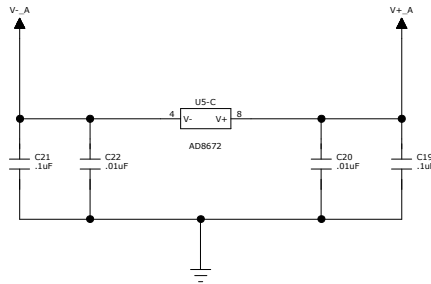
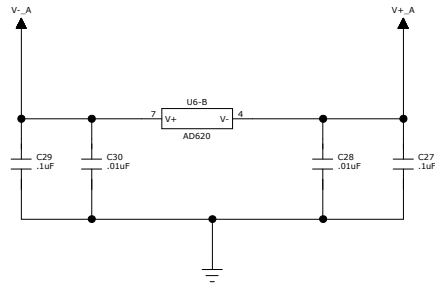
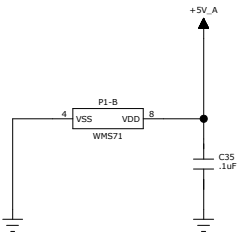
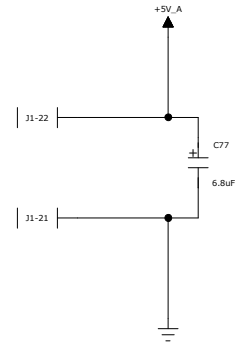
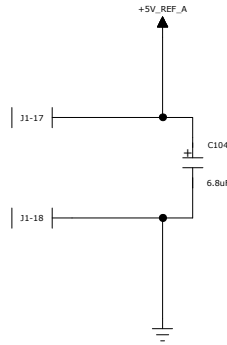
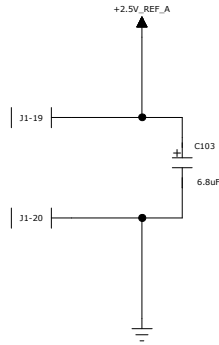
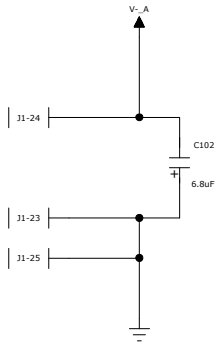
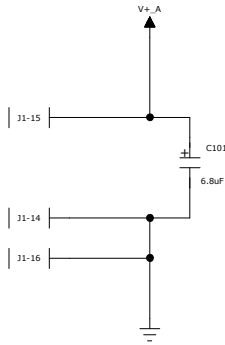
4

3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



set V+\_A between +6.5 to +7.5 VDC  
set V-\_A between -6.5 to -7.5 VDC

DRAWN:	D. Miller	DATED:	1/16/2007
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RELEASED:		DATED:	

COMPANY: California Institute of Technology			
TITLE: LNA daughter board			
CODE:	SIZE:	DRAWING NO:	REV:
	C		
SCALE:			SHEET: 2 of 4



6

5

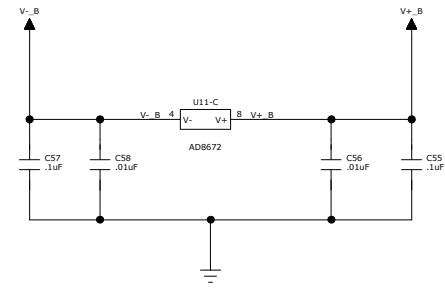
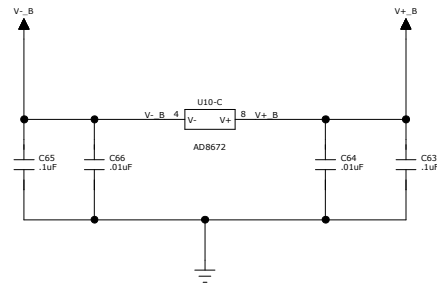
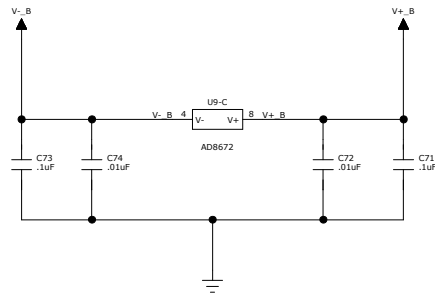
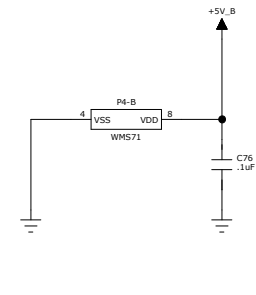
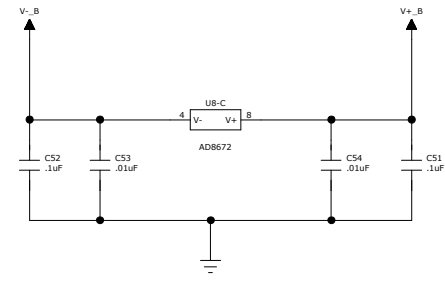
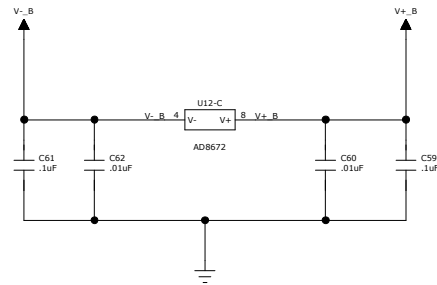
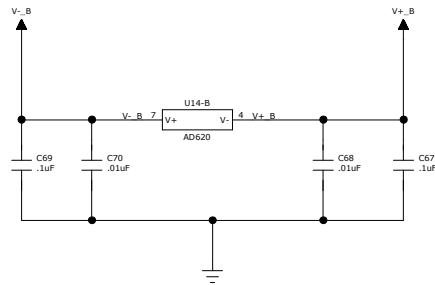
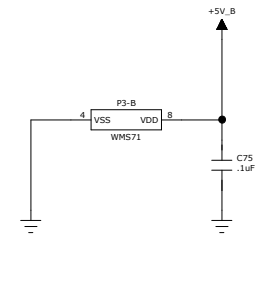
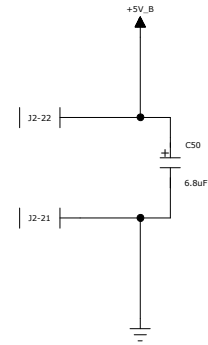
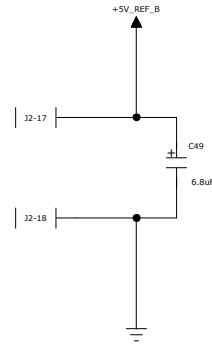
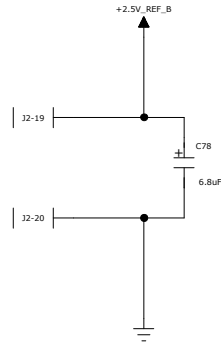
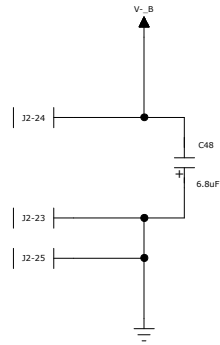
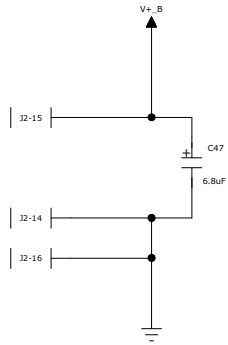
4

3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



set V+\_B between +6.5 to +7.5 VDC  
set V-\_B between -6.5 to -7.5 VDC

DRAWN:	D. Miller	DATED:	1/16/2007
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COMPANY: California Institute of Technology			
TITLE: LNA daughter board			
CODE:	SIZE: C	DRAWING NO:	REV:
SCALE:		SHEET: 4 of 4	